

ABSTRACT OF THE DISCLOSURE

5 A large-scale integrated-circuit apparatus  
includes a CPU and a plurality of circuit blocks to be  
operated in accordance with clock signals. The circuit  
blocks receive reset signals to perform initialization  
and output reset completion signals. The CPU logic-  
operates the initialization completion signals output  
from the circuit blocks and outputs a signal for  
canceling resetting to the circuit blocks in accordance  
10 with the logic-operation result.